

## Low voltage fast-switching NPN power transistor

#### **Features**

- AEC Q101 compliant
- Very low collector to emitter saturation voltage
- High current gain characteristic
- Fast-switching speed
- Surface-mounting SOT-223 power package in tape and reel

### **Applications**

High efficiency low voltage switching applications

### **Description**

The device is manufactured in planar technology with "Base Island" layout.

The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage.

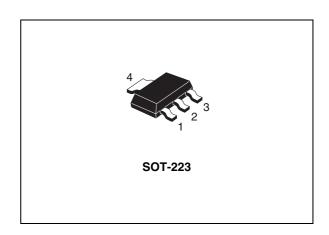


Figure 1. Internal schematic diagram

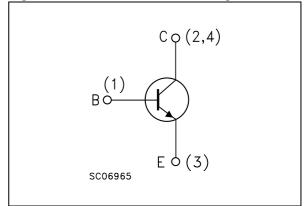


Table 1. Device summary

Order code	Marking	Package	Packaging
STN851-A	N851	SOT-223	Tape and reel

Electrical ratings STN851-A

# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)	150	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	60	V
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	7	V
I <sub>C</sub>	Collector current	5	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	10	Α
I <sub>B</sub>	Base current	1	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	2	Α
P <sub>tot</sub>	Total dissipation at T <sub>amb</sub> = 25 °C	1.6	W
T <sub>stg</sub>	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thj-amb</sub>	Thermal resistance junction-ambient (1)	78	°C/W

<sup>1.</sup> Device mounted on a p.c.b. area of 1  $\mbox{cm}^2$ 

## 2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C \text{ unless otherwise specified})$ 

Table 4. Electrical characteristics

Symbol	Parameter	Test co	nditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector cut-off current (I <sub>E</sub> = 0)	V <sub>CB</sub> = 120 V V <sub>CB</sub> = 120 V	T <sub>c</sub> = 100 °C			50 1	nΑ μΑ
I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 7 V				10	nA
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage (I <sub>E</sub> = 0)	I <sub>C</sub> = 100 μA		150			٧
V <sub>(BR)CEO</sub> <sup>(1)</sup>	Collector-emitter breakdown voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA		60			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage (I <sub>C</sub> = 0)	Ι <sub>Ε</sub> = 100 μΑ		7			V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_{C} = 100 \text{ mA}$ $I_{C} = 1 \text{ A}$ $I_{C} = 2 \text{ A}$ $I_{C} = 5 \text{ A}$	$I_B = 50 \text{ mA}$		10 70 140 320	50 120 250 500	mV mV mV
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	I <sub>C</sub> = 4 A	I <sub>B</sub> = 200 mA		1	1.15	V
V <sub>BE(on)</sub> (1)	Base-emitter on voltage	I <sub>C</sub> = 4 A	V <sub>CE</sub> = 1 V		0.89	1	V
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$I_{C} = 10 \text{ mA}$ $I_{C} = 2 \text{ A}$ $I_{C} = 5 \text{ A}$ $I_{C} = 10 \text{ A}$	$V_{CE} = 1 V$ $V_{CE} = 1 V$	150 150 90 30	300 270 140 50	350	
f <sub>T</sub>	Transition frequency	V <sub>CE</sub> = 10 V	I <sub>C</sub> = 100 mA		130		MHz
C <sub>CBO</sub>	Collector-base capacitance (I <sub>E</sub> = 0)	V <sub>CB</sub> = 10 V	f = 1 MHz		50		pF
t <sub>on</sub> t <sub>s</sub>	Resistive load Turn-on time Storage time Fall time	$I_C = 1 A$ $I_{B1} = -I_{B2} = 0.$			50 1.35 120		ns µs ns

<sup>1.</sup> Pulse duration = 300  $\mu s$ , duty cycle  $\leq 1.5\%$ 

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Electrical characteristics STN851-A

#### 2.1 Electrical characteristics (curves)

Figure 2. Derating curve

Figure 3. DC current gain

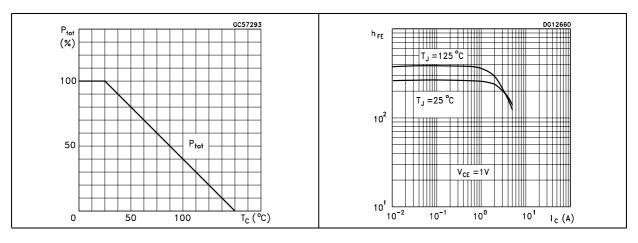


Figure 4. Collector-emitter saturation voltage

Figure 5. Collector-emitter saturation voltage

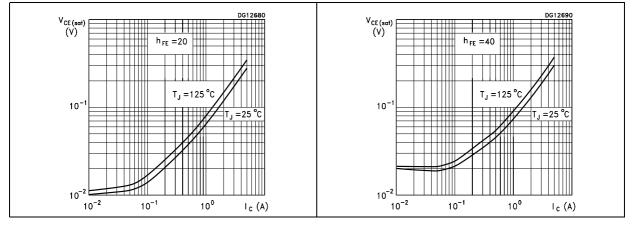
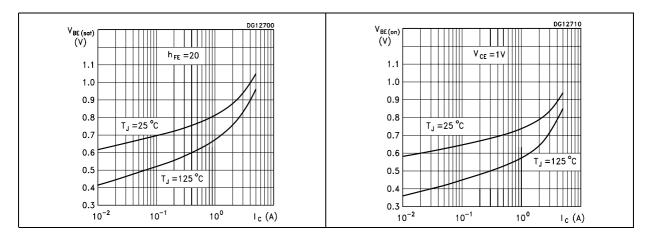


Figure 6. Base-emitter saturation voltage

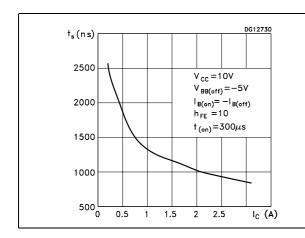
Figure 7. Base-emitter on voltage



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Figure 8. Resistive load switching time

Figure 9. Resistive load switching time



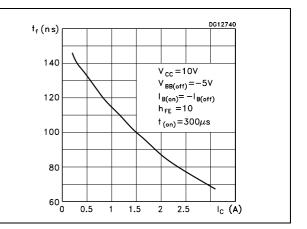
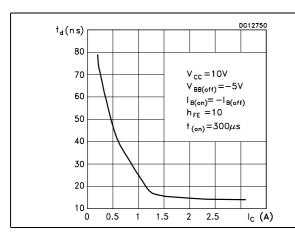


Figure 10. Resistive load switching time

Figure 11. Inductive load switching time



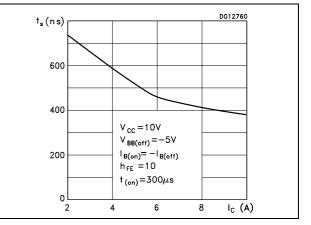
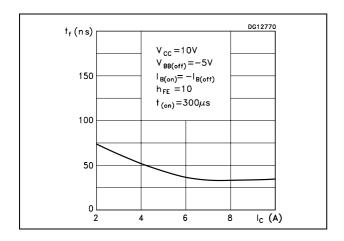


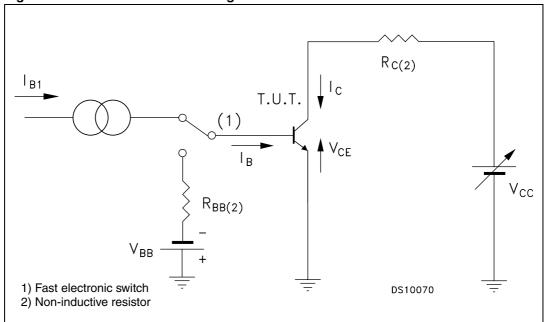
Figure 12. Inductive load switching time



Electrical characteristics STN851-A

## 2.2 Test circuit

Figure 13. Resistive load switching test circuit



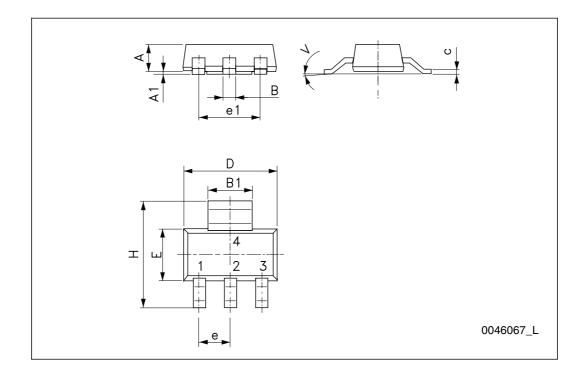
# 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and products status are available at: www.st.com. ECOPACK is an ST trademark.

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DIM.	mm.					
	min.	typ	max.			
Α			1.80			
A1	0.02		0.1			
В	0.60	0.70	0.85			
B1	2.90	3.00	3.15			
С	0.24	0.26	0.35			
D	6.30	6.50	6.70			
е		2.30				
e1		4.60				
E	3.30	3.50	3.70			
Н	6.70	7.00	7.30			
V			10 °			



STN851-A Revision history

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes
16-Mar-2009	1	First issue

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